Attorney Docket No.: YOR920030373US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re PATENT APPLICATION of:

Anthony Correale, Jr. et al.

Appln. No.: Unknown

Art Unit:

Filed:

herewith

Examiner: Unknown

For:

SINGLE SUPPLY LEVEL CONVERTER

INFORMATION DISCLOSURE STATEMENT

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Sir,

Pursuant to the duty of disclosure under 37 C.F.R. §1.56, submitted herewith is an Information Disclosure Statement (IDS) including a Form PTO-1449 and a copy of four (4) nonpatent publication. Pursuant to a Pre-OG Notice dated July 11, 2003 waiving requirement for copies of U.S. Patents and Published Applications, copies of Patents and Published Applications cited on the PTO-1449 are not included.

Since this IDS is being submitted with the application and prior to a first office action, no certification or fee is believed required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to all documents submitted. Consideration of the submitted documents is respectfully requested.

November 24, 2003

(Date)

Respectfully Submitted.

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INFORMATION DISCLOSURE				First Named Inventor	Correale, Jr. et al.	
STATEMENT BY APPLICANT		Art Unit				
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Sheet	1	of	2	Attorney Docket Number	YOR920030373US1	

U.S. PATENT DOCUMENTS							
Examiner Initials*	Cite No.1	Document Number Number - Kind Code ^{2 (# known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
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FOREIGN PATENT DOCUMENTS							
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Sheet	2	of	2	Attorney Docket Number	YOR920030359US1		

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite _i No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		LACKEY ET AL., Managing power and performance for System-on-Chip designs using Voltage Islands, Proceedings of the 2002 IEEE/ACM international conference on Computer-aided design , 11/11/2002, Page(s) 195-202	
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